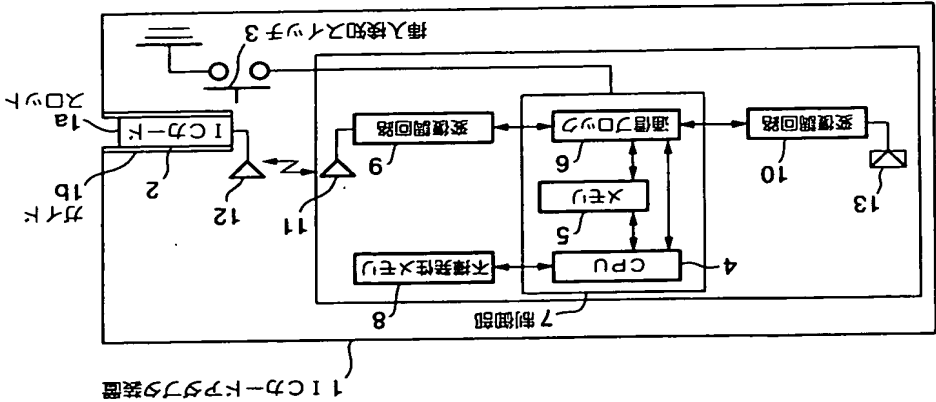


Fig. 1

- 1 IC CARD ADAPTER APPARATUS
- 1a SLOT
- 1b GUIDE
- 2 IC CARD
- 3 INSERTION DETECTING SWITCH
- 5 MEMORY
- 6 COMMUNICATION BLOCK
- 7 CONTROL UNIT
- 8 NONVOLATILE MEMORY
- 9 MODULATION AND DEMODULATION CIRCUIT
- 10 MODULATION AND DEMODULATION CIRCUIT



[図1]

Fig. 2

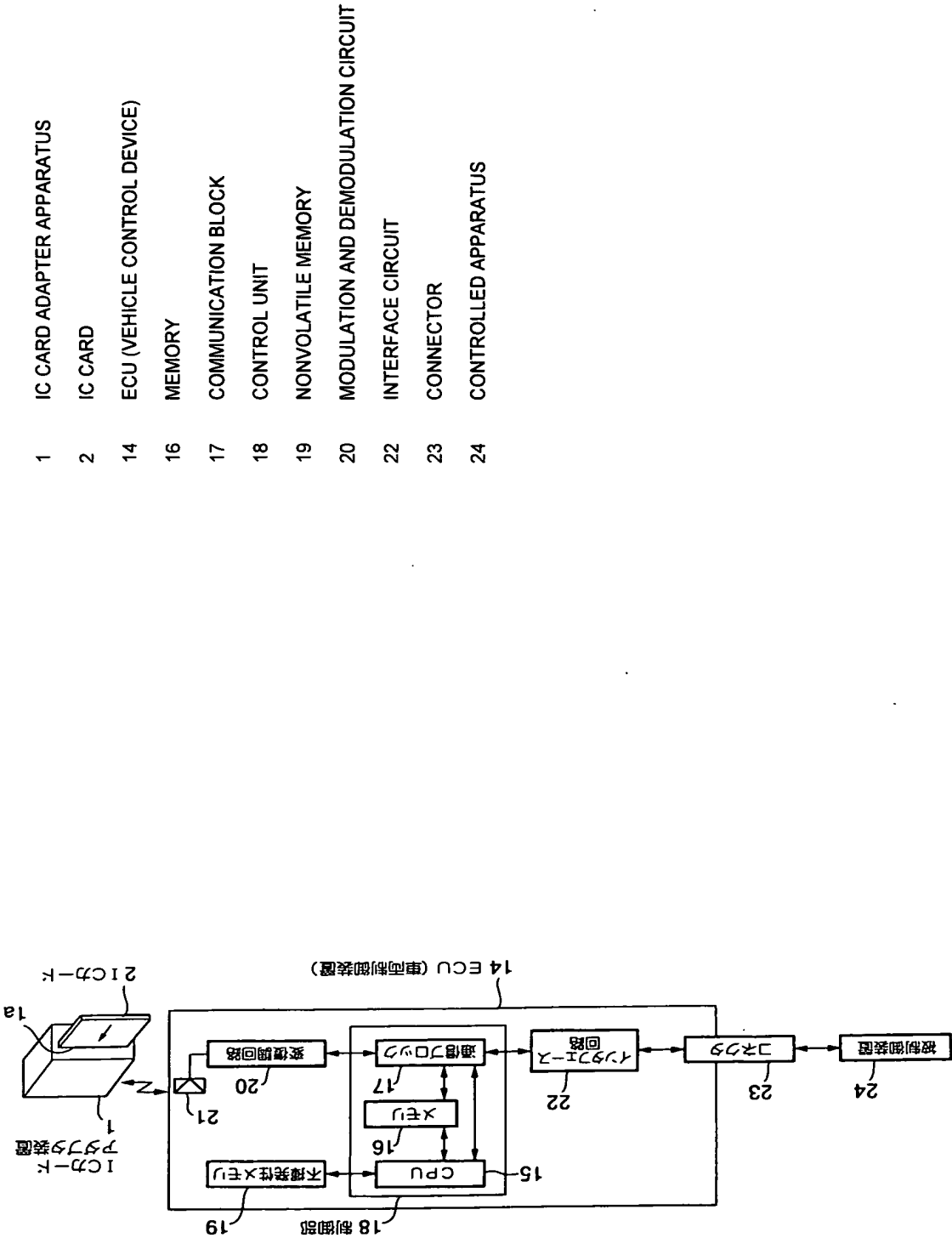
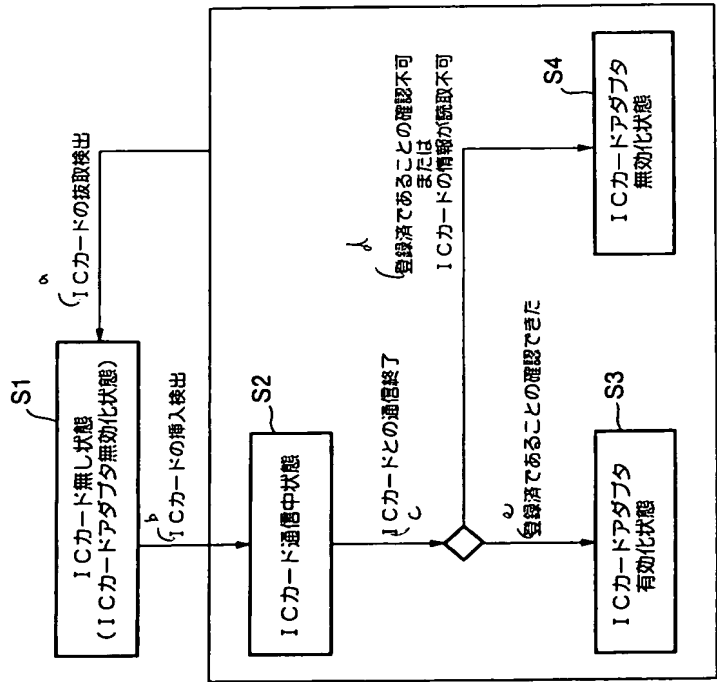
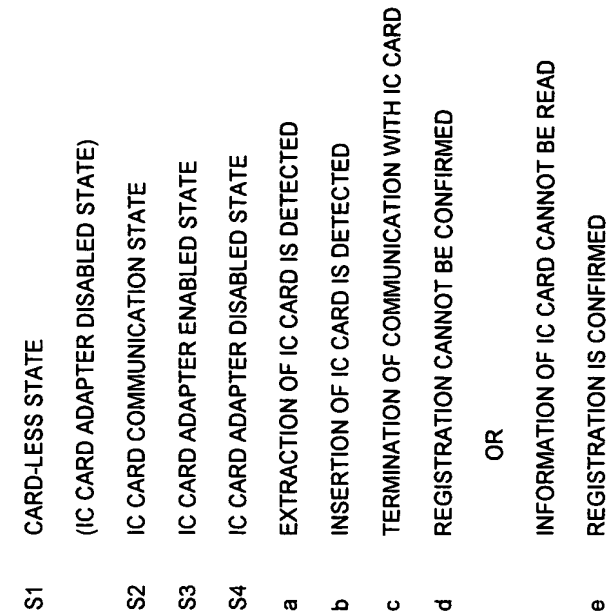


Fig. 3



[図3]

[図4]

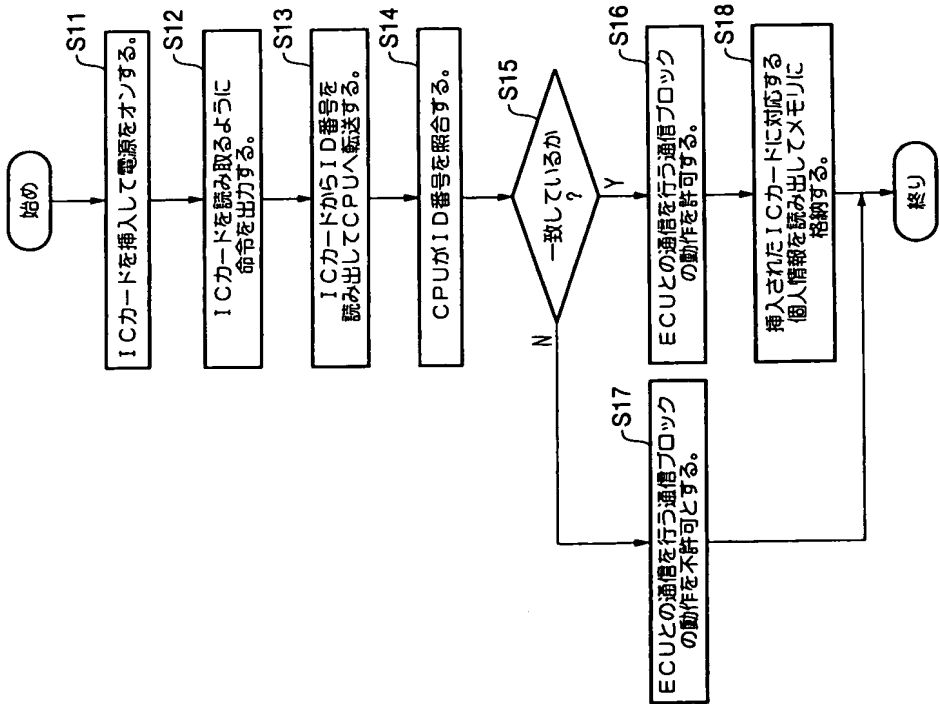
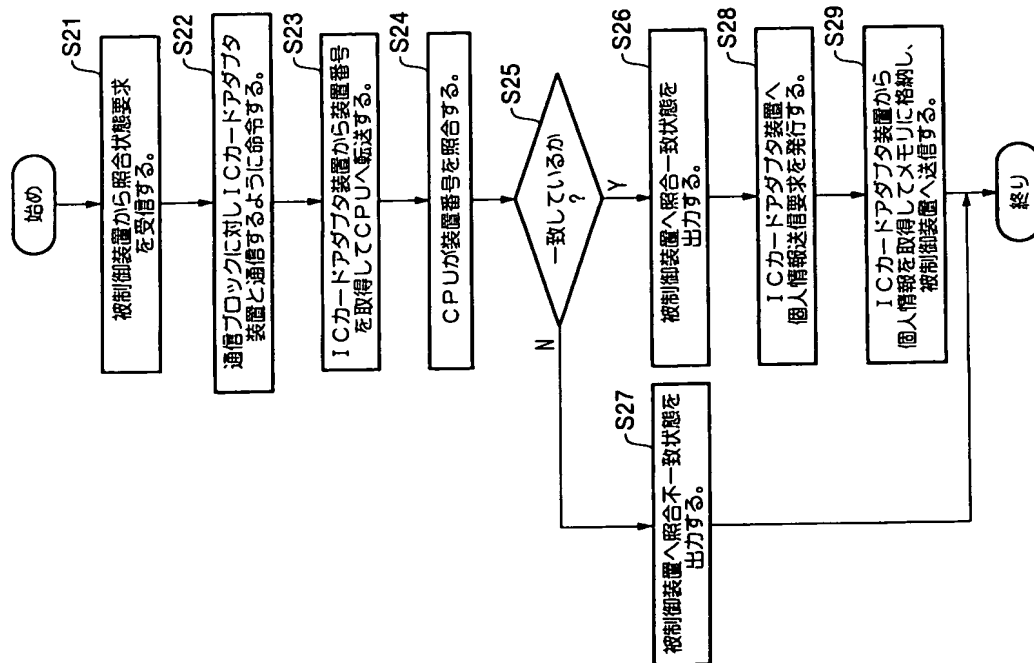


Fig. 4

START
S11 INSERT IC CARD AND TURN ON POWER
S12 OUTPUT INSTRUCTION TO READ IC CARD
S13 READ ID NUMBER FROM IC CARD AND TRANSFER IT TO CPU
S14 CPU COLLATES ID NUMBER
S15 COINCIDENT?
S16 ENABLE OPERATION OF COMMUNICATION BLOCK WHICH COMMUNICATES WITH ECU
S17 DISABLE OPERATION OF COMMUNICATION BLOCK WHICH COMMUNICATES WITH ECU
S18 READ PERSONAL INFORMATION CORRESPONDING TO INSERTED IC CARD, AND STORE IT IN MEMORY
END

[図5]

Fig. 5



START

S21 RECEIVE COLLATION STATE REQUEST FROM CONTROLLED APPARATUS

S22 INSTRUCT COMMUNICATION BLOCK TO COMMUNICATE WITH IC CARD ADAPTER APPARATUS

S23 OBTAIN APPARATUS NUMBER FROM IC CARD ADAPTER APPARATUS AND TRANSFER IT TO CPU

S24 CPU COLLATE APPARATUS NUMBER

S25 COINCIDENT?

S26 OUTPUT COLLATION COINCIDENCE STATE TO CONTROLLED APPARATUS

S27 OUTPUT COLLATION INCOINCIDENCE STATE TO CONTROLLED APPARATUS

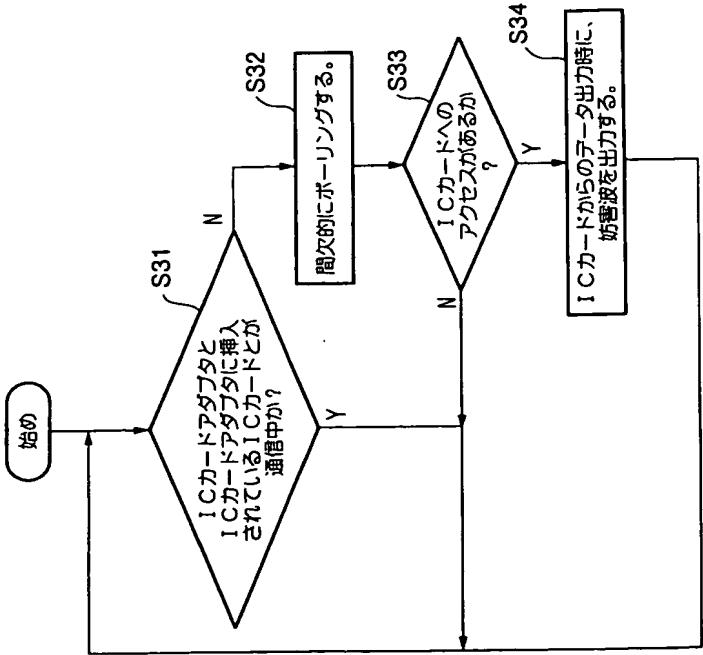
S28 ISSUE PERSONAL INFORMATION TRANSMISSION REQUEST TO IC CARD ADAPTER APPARATUS

S29 OBTAIN PERSONAL INFORMATION FROM IC CARD ADAPTER APPARATUS, STORE IT IN MEMORY, AND TRANSMIT IT TO CONTROLLED APPARATUS

END

Fig. 6

START
S31 IC CARD ADAPTER COMMUNICATES WITH IC CARD INSERTED
INTO IC CARD ADAPTER?
S32 INTERMITTENTLY CONDUCT POLLING
S33 IC CARD IS ACCESSED?
S34 OUTPUT INTERFERING WAVE WHEN DATA ARE OUTPUT FROM IC
CARD



[図6]